

Claims

- [c1] 1. A digital-to-analog converting circuit of a display, characterized in comprising a plurality of transistors, wherein the ratio of channel-width to channel-length of each transistor is identical.
- [c2] 2. The digital-to-analog converting circuit according to claim 1, further comprising a control device coupled to the transistors to receive a plurality of data bits, so as to control a conducting number among the transistors according to the data bits.
- [c3] 3. The digital-to-analog converting circuit according to claim 1, operative to generate a data current to drive a plurality of pixels of the display.
- [c4] 4. A digital-to-analog converting circuit, applied to a current-type data driver, comprising:
a plurality of transistors; and
a control device coupled to the transistors, the control device being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits;
wherein the ratio of the channel-width to channel-length for each of the transistors is the same.
- [c5] 5. The digital-to-analog converting circuit according to claim 4, being operative to generate a data current to drive a plurality of pixels of the display.